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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/961,176	09/24/2001	Ikuo Hiyama	500.40686X00	7190

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EXAMINER

PATEL, NITIN

ART UNIT	PAPER NUMBER
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2673

DATE MAILED: 03/11/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/961,176

Applicant(s)

HIYAMA ET AL.

Examiner

Nitin Patel

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 22 December 2003.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-22 is/are pending in the application.
- 4a) Of the above claim(s) 17-22 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-16 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

2. Claims 1-16 are rejected under 35 U.S.C. 102(e) as being anticipated by Hara (U.S. Patent No. 5,047,849).

As per claim 1, Hara shows an image display unit (display screen 10 In Fig.9); and a control unit for outputting the image information to the display unit (in Fig.9) wherein a block discrimination circuit portion for discriminating a state of image information amounting to one frame among the image information in a pixel block unit (in Fig.9 and In col.6 lines 62-67 to Col.7 lines 1-20); an image processing portion for processing the image information on the basis of the discriminated result of the block discrimination circuit portion; a storage portion (In Col.7 lines 1-20) for storing the image information processed by the image processing portion and a synchronizing signal (In Col.7 lines 2) generation portion for reading the image information from the storage portion, controlling the clock (in col.8 lines 13-20) in accordance with the read image information and outputting the image information to the display unit.

As per claims 2-5,7,9,10,12,15,16 Hara shows display system discriminates the state of the moving or still picture (1 and 0 signal for image in Col.8 lines 5-8) and process the image differently depending on the discriminated result of the state in the pixel block (in Fig.12 a-c) and process the image at a smaller number of gradation when the result is the moving picture and still picture by controlling with different clock (in col.8 lines 39-67).

As per claim 6, synchronizing signal generation portion generates a faster clock in the clock control when the result is for moving picture than when the result is still picture (in Fig.11).

As per claim 7, Hara shows an image display unit (display screen 10 In Fig.9); and a control unit for outputting the image information to the display unit (in Fig.9) wherein a block discrimination circuit portion for discriminating a state of image information amounting to one frame among the image information in a pixel block unit (in Fig.9 and In col.6 lines 62-67 to Col.7 lines 1-20); an image processing portion for processing the image information on the basis of the discriminated result of the block discrimination circuit portion; a storage portion(In Col.7 lines for storing the image information processed by the image processing portion and a synchronizing signal(In Col.7 lines 2) generation portion for reading the image information from the storage portion, controlling the clock (in col.8 lines 13-20)in accordance with the read image information and outputting the image information to the display unit.

As per claims 8,11,13,14 Hara shows an image display unit (display screen 10 In Fig.9); and a control unit for outputting the image information to the display unit (in

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Fig.9) wherein a block discrimination circuit portion for discriminating a state of image information amounting to one frame among the image information in a pixel block unit (in Fig.9 and In col.6 lines 62-67 to Col.7 lines 1-20); an image processing portion for processing the image information on the basis of the discriminated result of the block discrimination circuit portion; a storage portion(In Col.7 lines for storing the image information processed by the image processing portion and a synchronizing signal(In Col.7 lines 2) generation portion for reading the image information from the storage portion, controlling the clock (in col.8 lines 13-20)in accordance with the read image information and outputting the image information to the display unit.

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Nitin Patel whose telephone number is 703-308-7024. The examiner can normally be reached on 8:00-5:00.

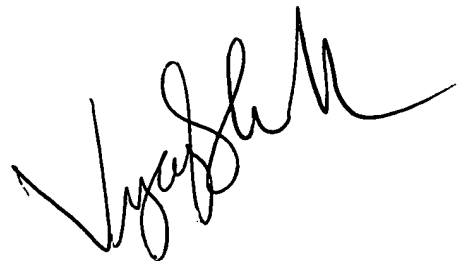
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin H Shalwala can be reached on 703-305-4938. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

NP

March 8, 2004



VIJAY SHANKAR
PRIMARY EXAMINER